



Attorney's Docket No.: 10559-079001 / P7569

2187
JFV

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Matthew J. Adiletta, et al. Art Unit : 2187
Serial No. : 09/387,109 Examiner : David Jesse Diller
Filed : August 31, 1999
Title : SDRAM CONTROLLER FOR PARALLEL PROCESSOR ARCHITECTURE

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL REPLY (37 CFR 1.111(a)(2)) TO ACTION OF SEPTEMBER 13, 2004

Please amend the above-identified application as follows:

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

1/31/05
Date of Deposit

Signature

Irja Zarembok

Typed or Printed Name of Person Signing Certificate